

cont. A1 reference resistor 29, the voltage developed across reference resistor 29 at any temperature is substantially the same as the voltage drop across data resistor 30 caused by current I_a flowing through data resistor 30. The input reference voltage is thus adjusted at a rate that maintains current I_a substantially constant through data resistor 30 when the associated word line is selected.

IN THE CLAIMS

Substitute claim 2:

- 1 2. (Amended) A temperature compensation circuit as recited in Claim 1 wherein
 A2 2
 3 electrical conductive properties of said reference resistor are selected to be
 the same as the electrical conductive properties of said data resistors.

Substitute claim 7:

- 1 7. (Amended) A temperature compensation circuit as recited in Claim 1 wherein
 A3 2
 3 conductive properties of said reference resistors are selected such that a change
 4 in electrical conductive properties of said reference resistors matches a change
 in electrical conductive properties of said data resistors.

Substitute claim 19:

- 1 19. (Amended) A method to maintain a current through Read-Only Memory (ROM)
 Sub 19 2
 3 substantially constant as temperature changes wherein said ROM employs a
 4 plurality of data resistors to provide electrical interconnections between a plurality
 of input lines and output lines, comprising the steps of:
 5 selecting a reference resistor wherein a change in electrical conductive
 6 properties of said reference resistor matches a change in electrical conductive
 A4 7 properties of said data resistor;
 8 supplying a reference voltage to said input lines, said reference voltage
 9 developed by supplying a constant current to said reference resistor, wherein
 10 said reference voltage is responsive to a change in temperature.